

Books

- 3 Event-triggered Transmission Protocol in Robust Control Systems Niladri Sekhar Tripathy, Indra Narayan Kar, Kolin Paul CRC Press 2022
- 2 Architecture Exploration of FPGA Based Accelerators for BioInformatics Applications, Sharat Chandra Varma Bogaraju, Kolin Paul, M Balakrishnan, Springer ISBN 978-981-10-0589-3 April 2016
- 1 Algebra of Majority Logic, Rajeswari D, Kolin Paul, M Balakrishnan, Springer(*in print*)

Referred Book Chapter

- 2 Yadav, G., Paul, K., Gauravaram, P. (2022). Vulnerability Management in IIoT-Based Systems: What, Why and How. In: Pal, S., Jadidi, Z., Foo, E. (eds) Secure and Trusted Cyber Physical Systems. Smart Sensors, Measurement and Instrumentation, vol 43. Springer, Cham https://doi.org/10.1007/978-3-031-08270-2_3
- 1 Kolin Paul and Sanjay Rajopadhye, " Back Propagation Algorithm: Achieving 5 GOPS on the Virtex-E" Book Chapter in FPGA Implementations of Neural Networks By Amos R. Omondi; Jagath C. Rajapakse (Eds.). 0-387-28485-0 March, 2006.

Refereed Journals

- 50 Vatsal Agarwal, Vijay Kumar, Swati, Rohan Chawla and Kolin Paul: "NetraDeep: An Integrated Deep Learning and Image Processing System for Precise Detection of Hard Exudates" , ACM Transactions on Computing for Healthcare, (accepted) 2024
- 49 Kirti Gupta, Bijaya Ketan Panigrahi, Anupam Joshi, Kolin Paul: Demonstration of Denial of Charging Attack on Electric Vehicle Charging Infrastructure and its consequences. Int. J. Crit. Infrastructure Prot. 46: 100693 (2024)
- 48 A Choudhury, B Mondal, K Paul, BK Sikdar Designing a Deep Neural Network engine for LLC block reuse prediction to mitigate Soft Error in Multicore Microelectronics Reliability 156, 115377
- 47 SW Kibret, K Paul, VJ Ribeiro : AI-Powered Security for IoT: A Blockchain Enabled Device Twin Approach IntechOpen 2023
- 46 Vaibhav Deorari, Debashish Chowdhury, Kolin Paul, Pritesh Srivastava, Ashish Duggal , Development of a branching logic questionnaire and a Computer Based Assessment Tool (COMBAT) for Android based smartphones for the diagnosis of primary headache disorders using International classification of Headache Disorders third version (ICHD3), CEPHALALGIA 2023

- 45 Vijay Kumar, Kolin Paul: Device Fingerprinting for Cyber-Physical Systems: A Survey. *ACM Comput. Surv.* 55(14s): 302:1-302:41 (2023)
- 44 Vijay Kumar, and Kolin Paul. Fundus Imaging-based Healthcare: Present and Future. *ACM Transactions on Computing in Healthcare* 4(3): 16:1-16:34 (2023). <https://doi.org/10.1145/3586580>
- 43 Vijay Kumar, Het Patel, Kolin Paul, and Shorya Azad. Deep Learning Assisted Retinopathy of Prematurity (ROP) Screening. *ACM Transactions in Computing in Healthcare* Accepted 4(3): 18:1-18:32 (2023) <https://doi.org/10.1145/3596223>
- 42 Vijay Kumar, Het Patel, Kolin Paul and Shorya Azad. Improved Blood Vessels Segmentation of Infant Retinal Image. In: et al. (eds) *Biomedical Engineering Systems and Technologies. BIOSTEC 2022. Communications in Computer and Information Science.* Springer, Berlin, Heidelberg (accepted 2023)
- 41 Avishek Choudhury, Brototi Mondal, Kolin Paul, Biplab K Sikdar: Energy Efficiency in Multicore Shared Cache by Fault Tolerance Using a Genetic Algorithm Based Block Reuse Predictor, *Microprocessors and Microsystems*, 2023
- 40 Astha Chawla, Prakhar Agrawal, Bijaya Ketan Panigrahi, Kolin Paul : Deep-Learning-Based Data-Manipulation Attack Resilient Supervisory Backup Protection of Transmission Lines. *Neural Computing Applications*, 2023
- 39 Saurabh Tewari, Anshul Kumar, Kolin Paul: Minimizing Off-Chip Memory Access for CNN Accelerators. *IEEE Consumer Electron. Mag.* 11(3): 95-104 (2022)
- 38 Geeta Yadav, Praveen Gauravaram, Arun Kumar Jindal, Kolin Paul: SmartPatch: A Patch Prioritization Framework. *Computers in Industry.* 137: 103595 (2022)
- 37 Astha Chawla, Animesh Singh, Prakhar Agrawal, Bijaya Ketan Panigrahi, Bhavesh R. Bhalja, Kolin Paul: Denial-of-Service Attacks Pre-Emptive and Detection Framework for Synchrophasor Based Wide Area Protection Applications. *IEEE Syst. J.* 16(1): 1570-1581 (2022)
- 36 Ekaterina Avershina, Priyanka Sharma, Arne M. Taxt, Harpreet Singh, Stephan A. Frye, Kolin Paul, Arti Kapil, Umaer Naseer, Punit Kaur, Rafi Ahmad, AMR-Diag: Neural network based genotype-to-phenotype prediction of resistance towards β -lactams in *Escherichia coli* and *Klebsiella pneumoniae*, *Computational and Structural Biotechnology Journal*, Volume 19, 2021
- 35 Rajesh Kedia, Shikha Goel, M. Balakrishnan, Kolin Paul, Rijurekha Sen: Design Space Exploration of FPGA-Based System With Multiple DNN Accelerators. *IEEE Embed. Syst. Lett.* 13(3): 114-117 (2021)
- 34 Vijay Kumar, Het Patel, Kolin Paul, A Surve, Shorya Azad. and R Chawla: DL-Assisted ROP Screening Technique. In: C. Gehin et al. (eds) *Biomedical Engineering Systems and Technologies. BIOSTEC 2021. Communications in Computer and Information Science* 1710, pp. 1-23, 2022. Springer, Berlin, Heidelberg. https://doi.org/10.1007/978-3-031-20664-1_13
- 33 Ahmed Hemani, Muhammad Shafique, Kolin Paul: Guest Editorial: Special Issue on Architectures and Design Methods for Neural Networks. *J. Signal Process. Syst.* 92(11): 1215-1217 (2020)
- 32 Geeta Yadav, Kolin Paul: Architecture and security of SCADA systems: A review. *Int. J. Crit. Infrastructure Prot.* 34: 100433 (2021)

- 31 Geeta Yadav, Kolin Paul, Alaa Allakany, Koji Okamura: "IoT-PEN: An E2E Penetration Testing Framework for IoT", J. Inf. Process. 28: 633-642 (2020)
- 30 Astha Chawla, Prakhar Agrawal, Animesh Singh, Bijaya Ketan Panigrahi, Kolin Paul, Bhavesh R. Bhalja: Denial-of-Service Resilient Frameworks for Synchrophasor-Based Wide Area Monitoring Systems. IEEE Computer 53(5): 14-24 (2020)
- 29 Rajeswari Devadoss, Kolin Paul, M. Balakrishnan: Equivalence Checking and Compaction of n-input Majority Terms Using Implicants of Majority. J. Electronic Testing 35(5): 679-694 (2019)
- 28 Samuel P Wedaj, Kolin Paul and Vinay J Ribeiro, "DADS: Decentralized Attestation for Device Swarms" ACM Transactions on Privacy and Security 22(3): 19:1-19:29 (2019)
- 27 Madhusudana Girija Sanal, Kolin Paul, Senthil Kumar, Nirmal Kumar Ganguly: "Artificial Intelligence and Deep Learning: the Future of Medicine and Medical Practice", Journal of The Association of Physicians of India, Vol. 67, May 2019
- 26 Pei Liu, Ahmed Hemani, Kolin Paul, Christian Weis, Matthias Jung, Norbert Wehn: "3D-Stacked Many-Core Architecture for Biological Sequence Analysis Problems". International Journal of Parallel Programming 45(6): 1420-1460 (2017)
- 25 Sharat Chandra Varma Bogaraju, Kolin Paul, Balakrishnan M, Dominique Lavenier, "Hardware Acceleration of De Novo Genome Assembly", International Journal of Embedded Systems, 74-89 (2017)
- 24 Pei Liu, Ahmed Hemani, Kolin Paul, Christian Weis, Matthias Jung, Norbert Wehn: "3D-Stacked Many-Core Architecture for Biological Sequence Analysis Problems". International Journal of Parallel Programming 45(6): 1420-1460 (2017)
- 23 Niladri Sekhar Tripathy, I. N. Kar, Kolin Paul: "Stabilization of Uncertain Discrete-Time Linear System With Limited Communication". IEEE Trans. Automat. Contr. 62(9): 4727-4733 (2017)
- 22 Pei Liu, Ahmed Hemani, Kolin Paul, Christian Weis, Matthias Jung, Norbert Wehn, "A Customized Many-Core Hardware Acceleration Platform for Short Read Mapping Problems Using Distributed Memory Interface with 3D-Stacked Architecture" Signal Processing Systems 87(3): 327-341 (2017)
- 21 Niladri Tripathi, I N Kar and Kolin Paul, "Model Based Robust Control Law for Linear Event-triggered System", Asian Journal of Control December 2015 (online) DOI: 10.1002/asjc.1276
- 20 Avval Gupta, Anju Kansal, Kolin Paul and Sanjiva Prasad, "A Modular Android based Multi Sensor mHealth System" Springer Communications in Computer and Information Science Volume 511 of the series Communications in Computer and Information Science pp 360-377 January 2016
- 19 Rajesh Kumar Pal, Ierum Shanaya, Kolin Paul and Sanjiva Prasad, "Dynamic Core Allocation for Energy-Efficient Video Decoding on Embedded Multicore Platforms", Future Generation Computer Systems Springer 56, 247-261, 2016
- 18 Tripathi M, Deo RC, Suri A, Srivastav V, Baby B, Kumar S, Kalra P, Banerjee S, Prasad S, Paul K, Roy TS, Lalwani S. "Design and Validation of an Open Source Partial Task Trainer for

Endonasal Neuro-endoscopic Skills Development: Indian Experience", World Neurosurgery. 2016 Feb;86:259-69. doi: 10.1016/j.wneu.2015.09.045. Epub 2015 Sep 26.

- 17 Arun Parakh, M. Balakrishnan, Kolin Paul, "Improving Map-Reduce for GPUs with cache", International Journal of High Performance System Architecture (IJHPSA) Volume 5 Issue 3, July 2015
- 16 Syed Mohammad Asad Hassan Jafri, Ahmed Hemani, Kolin Paul, Juha Plosila and Hannu Tenhunen "Polymorphic Configuration Architecture for Coarse Grained Reconfigurable Architectures" IEEE Transactions on Very Large Scale Integration (VLSI) Systems Page 403-407 Jan. 2016
- 15 Syed Mohammad Asad Hassan Jafri, Ozan Ozbag, Nasim Farahini, Ahmed Hemani, Kolin Paul, Juha Plosila and Hannu Tenhunen "Architecture and Implementation of Dynamic Parallelism, Voltage and Frequency Scaling on CGRAs" ACM Journal on Emerging Technologies in Computing Systems (JETC) - Special Issue on Neuromorphic Computing and Emerging Many-Core Systems for Exascale Computing, Volume 11 Issue 4, April 2015
- 14 Anusha Subhramony Iyer and Kolin Paul, "Self-Assembly: A Review of Scope and Applications" IET Nanobiotechnology. June 2015 ;9(3):122-35.
- 13 Nasim Farahini, Ahmed Hemani, Hasan Sohofi, Syed Mohammad Asad Hassan Jafri, Muhammad Adeel Tajammul, Kolin Paul "Parallel Distributed Scalable Address Generation Scheme for a Coarse Grain Reconfigurable Computation and Storage Fabric", Microprocessors and Microsystems - Embedded Hardware Design 38(8): 788-802 (2014)
- 12 Rajesh Kumar Pal, Kolin Paul and Sanjiva Prasad, "A High Performance Reconfigurable ManyCore Architecture", J. Parallel Distrib. Comput. 74(11): 3071-3086 (2014)
- 11 Manish Kumar Jaiswal, Ray C.C. Cheung, M. Balakrishnan and Kolin Paul, "Unified Architecture for Double / Two-Parallel Single Precision Floating Point Adder", IEEE Trans. on Circuits and Systems 61-II(7): 521-525 (2014)
- 10 Manish Kumar Jaiswal, Ray C.C. Cheung, M. Balakrishnan and Kolin Paul, "Series Expansion based Efficient Architectures for Double Precision Floating Point Division", Journal of Circuits, Systems and Signal Processing. November 2014, Volume 33, Issue 11, pp 3499-3526.
- 9 Syed Mohammad Asad Hassan Jafri, Muhammad Adeel Tajammul, Kolin Paul, Ahmed Hemani, Juha Plosila and Hannu Tenhunen, "Private configuration environments (PCE) for efficient reconfiguration, in CGRAs", Springer Design and Automation of Embedded Systems, 2014.
- 8 Syed Mohammad Asad Hassan Jafri, Liang Guang, Ahmed Hemani, Juha Plosila, Kolin Paul and Hannu Tenhunen, "Energy-aware Fault-tolerant Network-on-chips for Addressing Multiple Traffic Classes", Microprocessors and Microsystems - Embedded Hardware Design 37(8-A): 811-822 (2013) *Number of citations:3*
- 7 Ashish Suri, Martin Bettag, Manjul Tripathi, Rama Chandra Deo, Tara Sankar Roy, Sanjeev Lalwani, Christoph Buser, Marcus Mehltz, Payal Jotwani, Britty Baby, Vinkle Srivastav, Ramandeep Singh, Subodh Kumar, Prem Kalra, Subhashis Banerjee, Kolin Paul, Sanjiva Prasad, Bhawani Sharma. "Simulation in Neurosurgery in India- NETS" CNS Quarterly, Summer 2014

- 6 Tripathi M, Deo RC, Suri A, Srivastav V, Baby B, Kumar S, Kalra P, Banerjee S, Prasad S, Paul K, Roy TS, Lalwani S. "Quantitative Analysis of Kawase's Triangle versus Modified Dolenc Kawase Rhomboid Approach for Middle Cranial Fossa Lesions with Variable Antero-posterior Extension". *Journal of Neurosurg* Jul 2015;123(1):14-22. doi: 10.3171/2015.2.JNS132876. Epub 2015 Apr 3.
- 5 Jotwani P, Srivastav V, Tripathi M, Deo RC, Baby B, Damodaran N, Singh R, Suri A, Bettag M, Roy TS, Busert C, Mehlitz M, Lalwani S, Garg K, Paul K, Prasad S, Banerjee S, Kalra P, Kumar S, Sharma BS, Mahapatra AK. "Free-access Open-source e-Learning in Comprehensive Neurosurgery Skills Training". *Neurology India*, Jul-Aug 2014 ;62(4):352-61. doi: 10.4103/0028-3886.141208.
- 4 Tapas Kumar Kundu, Kolin Paul, "Improving Android Performance and Energy Efficiency," *Journal of Low Power Electronics* 7(4): 516-528 (2011)
- 3 Rajeswari Devadoss, Kolin Paul, M. Balakrishnan: p-QCA: "A Tiled Programmable Fabric Architecture Using Molecular Quantum-Dot Cellular Automata", *ACM Journal on Emerging Technologies in Computing Systems* 7(3): 13 2011 *Number of citations:7*
- 2 R. Devadoss, K. Paul, and M. Balakrishnan. "Coplanar QCA crossovers", *Electron. Letters* 45, 1234 (2009) *Number of citations:10*
- 1 K. Paul, D. R. Chowdhury, and P. P. Chaudhuri, "Theory of Extended Linear Machines", *IEEE Transactions on Computers*, September 2002 *Number of citations:12*.

[Journals \(under Review\)](#)

- 1 Syed Mohammad Asad Hassan Jafri, Muhammad Adeel Tajammul, Peter Ellerve, Ahmed Hemani, Kolin Paul, Hannu Tenhunen and Juha Plosila, "Morphable Compression Architecture for Efficient Configuration in CGRAs", *Microprocessors and Microsystems*.

[arXiv](#)

- 5 Geeta Yadav, Kolin Paul: Architecture and Security of SCADA Systems: A Review. *CoRR abs/2001.02925* (2020)
- 4 Xinhui Lai, Maksim Jenihhin, Georgios N. Selimis, Sven Goossens, Roel Maes, Kolin Paul: Early RTL Analysis for SCA Vulnerability in Fuzzy Extractors of Memory-Based PUF Enabled Devices. *CoRR abs/2008.08409* (2020)
- 3 Xinhui Lai, Maksim Jenihhin, Jaan Raik, Kolin Paul: PASCAL: Timing SCA Resistant Design and Verification Flow. *CoRR abs/2002.11108* (2020)
- 2 Sourav Das, Samuel Wedaj, Kolin Paul, Umesh Bellur, Vinay Joseph Ribeiro: Airmed: Efficient Self-Healing Network of Low-End Devices. *CoRR abs/2004.12442* (2020)
- 1 Niladri Sekhar Tripathy, I. N. Kar, Kolin Paul, "Model Based Robust Control Law for Linear Event-triggered System", *arXiv:1412.8365v1*

Referred Conferences/Workshops

- 115 Vijay Kumar, Goldy, Kolin Paul, Mahesh Chowdhary: Long Short Term Memory (LSTM)-based Cuffless Continuous Blood Pressure Monitoring. VLSID 2024: 330-335
- 114 Avishek Choudhury, Brototi Mondal, Kolin Paul, Biplab K. Sikdar: LLC Block Reuse Predictor Design using Deep Learning to Mitigate Soft Error in Multicore. VLSID 2024: 690-695
- 113 Suyash Saxena, Varun Singh Negi, Kolin Paul: Compression of Large LSTM Networks for Inference on Space Constraint Systems. PReMI 2023: 137-146
- 112 Vijay Kumar, Vatsal Agrawal, Shorya Azad: Deep Learning Assisted Plus Disease Screening Retinal Image of Infants. In Proceedings of the 16th International Joint Conference on Biomedical Engineering Systems and Technologies - HEALTHINF, ISBN 978-989-758-631-6; ISSN 2184-4305, SciTePress, pages 538-545.
- 111 Vijay Kumar, Het Patel, Kolin Paul, Abhidnya Surve, Shorya Azad, Rohan Chawla: Improved Blood Vessels Segmentation of Retinal Image of Infants. HEALTHINF 2022: 142-153
- 110 Saurabh Tewari, Anshul Kumar, Kolin Paul: SACC: Split and Combine Approach to Reduce the Off-chip Memory Accesses of LSTM Accelerators. DATE 2022: 580-583
- 109 Vijay Kumar, Het Patel, Kolin Paul, Abhidnya Surve, Shorya Azad, Rohan Chawla: Deep Learning Assisted Retinopathy of Prematurity Screening Technique. HEALTHINF 2021: 234-243
- 108 Yu Yang, Ahmed Hemani, Kolin Paul: Scheduling Persistent and Fully Cooperative Instructions. DSD 2021: 229-237
- 107 Yu Yang, Ahmed Hemani, Kolin Paul: Scheduling Persistent and Fully Cooperative Instructions. FCCM 2021: 274
- 106 Vijay Kumar, Kolin Paul: DevFing: Robust LCR Based Device Fingerprinting. MECO 2021: 1-6
- 105 Geeta Yadav, Kolin Paul: Global Monitor using SpatioTemporally Correlated Local Monitors. NCA 2021: 1-10
- 104 Geeta Yadav, Kolin Paul, Alaa M. Allakany, Koji Okamura: IoT-PEN: A Penetration Testing Framework for IoT. ICOIN 2020: 196-201
- 103 B. Shanker Jaiswal, B. Chandra, Kolin Paul: Geodetic Distance and Dynamic Outlier Exclusion in EM Optimization of Self Exciting Point Process for Homicide Prediction in Chicago. IIAI-AAI 2020: 534-541
- 102 Saurabh Tewari, Anshul Kumar, Kolin Paul: "Bus Width Aware Off-Chip Memory Access Minimization for CNN Accelerators", ISVLSI 2020 ISVLSI 2020: 54-59
- 101 Ameer Shalabi, Kolin Paul, Tara Ghasempouri, Jaan Raik: "NV-SP: A New High Performance and Low Energy NVM-Based Scratch Pad", ISVLSI 2020 ISVLSI 2020: 240-245
- 100 Xinhui Lai, Maksim Jenihhin, Georgios N. Selimis, Sven Goossens, Roel Maes, Kolin Paul: Early RTL Analysis for SCA Vulnerability in Fuzzy Extractors of Memory-Based PUF Enabled Devices. VLSI-SOC 2020: 16-21

- 99 Alaa M. Allakany, Geeta Yadav, Kolin Paul, Koji Okamura: Detection and Mitigation of LFA Attack in SDN-IoT Network. AINA Workshops 2020: 1087-1096
- 98 Tara Ghasempouri, Jaan Raik, Kolin Paul, Cezar Reinbrecht, Said Hamdioui, Mottaqiallah Taouil: A Security Verification Template to Assess Cache Architecture Vulnerabilities. DDECS 2020: 1-6
- 97 Geeta Yadav, Kolin Paul, Alaa M. Allakany, Koji Okamura: IoT-PEN: A Penetration Testing Framework for IoT. ICOIN 2020: 196-201
- 96 Rajesh Kedia, M. Balakrishnan, Kolin Paul: A case for design space exploration of context-aware adaptive embedded systems: work-in-progress. CODES+ISSS 2019: 12:1-12:2
- 95 Rajesh Kedia, M. Balakrishnan, Kolin Paul: GRanDE: Graphical Representation and Design Space Exploration of Embedded Systems. DSD 2019: 4-12
- 94 Geeta, Kolin Paul: "PatchRank: Ordering updates for SCADA systems", 24th IEEE Conference on Emerging Technologies and Factory Automation 2019, Zargoza, Spain
- 93 Geeta Yadav, Kolin Paul: Assessment of SCADA System Vulnerabilities. ETFA 2019: 1737-1744
- 92 Geeta Yadav, Alaa M. Allakany, Vijay Kumar, Kolin Paul, Koji Okamura: Penetration Testing Framework for IoT. IIAI-AAI 2019: 477-482
- 91 Xinhui Lai, Maksim Jenihhin, Jaan Raik, Kolin Paul, "PASCAL: Timing SCA Resistant Design and Verification Flow" IOLTS2019, Greece
- 90 Dimitrios Stathis, Yu Yang, Saurabh Tewari, Ahmed Hemani, Kolin Paul, Manfred Grabherr and Rafi Ahmad, " Approximate Computing Applied to Bacterial Genome Identification using Self-Organizing Maps", ISVLSI2019
- 89 Rajeswari Devadoss, Kolin Paul, M Balakrishnan, "Majority Logic: Prime Implicants and n-Input Majority Term Equivalence", VLSID 2019
- 88 Sapna Sapna, N. S. Sreenivasalu, Kolin Paul:" DAPP: Accelerating Training of DNN". HPCC/SmartCity/DSS 2018: 867-872
- 87 Yu Yang, Dimmitrios Stathis, Prashant Sharma, Kolin Paul, Ahmed Hemani, Manfred Grabherr, Rafi Ahmad: RiBoSOM: rapid bacterial genome identification using self-organizing map implemented on the synchoros SiLago platform. SAMOS 2018: 105-114
- 86 Varan Gupta, Rohit Patel, Gaurav Sardal, Jyotirmoy Ray, S. K. Saha, Kolin Paul: Design and Development of Robots for ABU Robocon 2016. AIR 2017: 13:1-13:6
- 85 Syed Mohammad Asad Hassan Jafri, Ahmed Hemani, Kolin Paul, Naeem Abbas , "MOCHA: Morphable Locality and Compression Aware Architecture for Convolutional Neural Networks". IPDPS 2017: 276-286
- 84 Chinmaya Dash, Kolin Paul and D Roy Chowdhury, "AES in Partially Reconfigurable CGRAs", TENCON 2016, Singapore
- 83 Naman Agarwal and Kolin Paul, "XEBRA: XEN Based Remote Attestation", TENCON 2016, Singapore.

- 82 Shalini Singh , Kolin Paul, Geeta and Sanjiva Prasad, "prasavGraph: Android based Labor Monitoring" HEALTHINF 2016 Rome, Italy.
- 81 Kolin Paul and Vijay Kumar, " mNetra: Fundus Imaging based Retinoscopy", HEALTHINF 2016 Rome, Italy.
- 80 Kolin Paul, Abdul Khalid, Yasoob Haider, Shalini Singh and Sanjiva Prasad, "prasavGraph: an Android-based e-Partograph", ICSMB India, 2016
- 79 Pei Liu, Kolin Paul, Ahmed Hemani, "3D-stacked Many-Core Architecture for Biological Sequence Analysis Problems", SAMOS 2015.
- 78 Rajeswari Devadoss, Kolin Paul and M Balakrishnan, "An n-input Majority Algebra based Logic Synthesis Tool for Quantum-dot Cellular Automata", 25th International Workshop on Logic & Synthesis Austin, USA 2015.
- 77 Mansureh S, Kolin Paul M Balakrishnan, "Partial Reconfiguration for Dynamic Mapping of Task Graphs onto 2D Mesh Platform" 11th International Symposium on Applied Reconfigurable Computing Bochum, Germany 2015
- 76 Kolin Paul and Vijay Kumar, " Fundus Imaging Based Affordable Eye Care", HEALTHINF 2014
- 75 Niladri Tripathi, I N Kar and Kolin Paul, "A Model Based Robust Control Law for Linear Event-triggered System", INDICON, Pune 2014
- 74 Niladri Tripathi, I N Kar and Kolin Paul, "An Event-triggered Based Robust Control of Robot Manipulator", ICARCV 2014 Singapore.
- 73 Rajesh Kumar Pal, Kolin Paul and Sanjiva Prasad, "Energy Efficient Dynamic Core Allocation for Video Decoding in Embedded Multicore Architectures", IEEE ICES- 2014.
- 72 Sharat Chandra Varma Bogaraju, Kolin Paul and M Balakrishnan, "High Level Design Approach to Accelerate De Novo Genome Assembly using FPGAs", EUROMICRO DSD 2014, Italy.
- 71 Syed Mohammad Asad Hassan Jafri, Muhammad Adeel Tajammul, Peter Ellerve, Ahmed Hemani, Kolin Paul, Hannu Tenhunen and Juha Plosila, "Morphable Compression Architecture for Efficient Configuration in CGRAs", EUROMICRO DSD 2014, Italy.
- 70 Syed Mohammad Asad Hassan Jafri, Masoud Daneshtalab, Kolin Paul, Ahmed Hemani, Hannu Tenhunen, Guillermo Serreno and Naeem Abbas "TransPar: Transformation Based Dynamic Parallelism for Low Power CGRAs", FPL 2014
- 69 Manish Kumar Jaiswal, Ray C.C. Cheung, M. Balakrishnan and Kolin Paul, "Configurable Architecture for Double / Two-Parallel Single Precision Floating Point Division", ISVLSI 2014
- 68 Syed Mohammad Asad Hassan Jafri, Guillermo Serrano, Junaid Iqbal, Masoud Daneshtalab, Ahmed Hemani, Kolin Paul, Juha Plosila and Hannu Tenhunen, "RuRot: Run-time Rotatable-expandable Partitions for Efficient Mapping in CGRAs" SAMOS 2014
- 67 B. Sharat Chandra Varma, Kolin Paul, M. Balakrishnan, "High Level Design Approach to Accelerate De Novo Genome Assembly using FPGAs", ASAP 2014 (accepted but withdrawn for logistical reasons)

- 66 Mansureh S, Kolin Paul M Balakrishnan "Mapping Tasks to a Dynamically Reconfigurable Coarse Grained Array" FCCM Toronto 2014
- 65 Syed Mohammad Asad Hassan Jafri, Kolin Paul, Masoud Daneshtalab, Ahmed Hemani, Juha Plosila, and Hannu Tenhunen "Morphable compression architecture for efficient configuration in CGRAs" FCCM Toronto 2014
- 64 Avval Gupta, Anjua Kansal, Kolin Paul and Sanjiva Prasad, "mDROID - An Affordable Android based mHealth System", HEALTHINF 2014
- 63 Kolin Paul and Chinmaya Dash, "A Reconfigurable MultiProcessor Framework with A Fast Overlay Network", VLSI Design 2014
- 62 B. Sharat Chandra Varma, Kolin Paul, M. Balakrishnan, "Accelerating Genome Assembly using Hard Embedded Blocks in FPGAs", VLSI Design 2014
- 61 Syed M. A. H. Jafri and Stanislaw J. Piestra and Ahmed Hemani and Kolin paul and Juha Plosila and Hannu Tenhunen, "Implementation and evaluation of configuration scrubbing on CGRAs: A case study", International Symposium on System-on-Chip, Finland 2013
- 60 Ashutosh Jain, Anshuj Garg and Kolin Paul, "GAGM: Genome Assembly on GPU using Mate pairs", 20th Annual International Conference on High Performance Computing (HiPC 2013), Bangalore 2013
- 59 Anshuj Garg, Ashutosh Jain and Kolin Paul, "GGAKE: GPU based Genome Assembly using K-mer Extension", 15th IEEE International Conference on High Performance Computing and Communications (HPCC 2013), Zhangjiajie, China, November 13-15, 2013
- 58 Nasim Farhani, Kolin Paul and Ahmed Hemani, "Distributed Runtime Computation of Constraints for Multiple Inner Loops", EUROMICRO DSD 2013 Spain
- 57 SMAH Jafri, O Bag, A Hemani, N Farahini, Kolin Paul, J Plosila, H Tenhunen, "Energy-aware coarse-grained reconfigurable architectures using dynamically reconfigurable isolation cells", 4th International Symposium on Quality Electronic Design (ISQED), 2013. *Number of citations:15*
- 56 Syed Mohammad Asad Hassan Jafri, Liang Guang, Ahmed Hemani, Juha Plosila, Kolin Paul and Hannu Tenhunen, "Energy-Aware Fault-Tolerant CGRAs Addressing Application with Different Reliability Needs", EUROMICRO DSD 2013 Spain.
- 55 B. Sharat Chandra Varma, Kolin Paul, M. Balakrishnan and Dominique Lavenier, " FAssem : FPGA based Acceleration of De Novo Genome Assembly" FCCM 2013 Washington USA. *Number of citations:7*
- 54 Mansureh S, Kolin Paul M Balakrishnan: Design and Implementation of High Performance Architectures with Partially Reconfigurable CGRAs RAW 2013 Boston.
- 53 Nidhi U, Kolin Paul, Anshul Kumar Ahmed Hemani: High Performance 3D-FFT Implementation. ISCAS 2013 Beijing
- 52 Syed Mohammad Asad Hassan Jafri, Liang Guang, Ahmed Hemani, Juha Plosila, Kolin Paul and Hannu Tenhunen, "Energy-Aware-Task-Parallelism for Efficient Dynamic Voltage, and Frequency Scaling, in CGRAs", SAMOS XIII 2013 Greece. *Number of citations:13*

- 51 B. Sharat Chandra Varma, Kolin Paul, M. Balakrishnan: Accelerating 3D-FFT Using Hard Embedded Blocks in FPGAs. VLSI Design 2013: 92-97
- 50 Aarathi Prasad, Ronald A. Peterson, Shrirang Mare, Jacob Sorber, Kolin Paul, David Kotz: Provenance framework for mHealth. COMSNETS 2013: 1-6 *Number of citations:9*
- 49 Syed Mohammad Asad Hassan Jafri, Liang Guang, Ahmed Hemani, Juha Plosila, Kolin Paul and Hannu Tenhunen, "Energy-aware Fault-tolerant Network-on-chips for Addressing Multiple Traffic Classes", EUROMICRO DSD 2012 Turkey.
- 48 Kolin Paul, Chinmaya Dash and Mansureh Moghaddam, "reMORPH – A Runtime Reconfigurable Architecture", EUROMICRO DSD 2012, Turkey.*Number of citations:4*
- 47 Rajesh Kumar Pal, Kolin Paul, Sanjiva Prasad, "ReKonf: A Reconfigurable Adaptive ManyCore Architecture", Parallel and Distributed Processing with Applications (ISPA), 2012 IEEE 10th International Symposium on , vol., no., pp.182-191, 10-13 July 2012 *Number of citations:4*
- 46 Arun Parakh, M. Balakrishnan, Kolin Paul, "Performance estimation for GPUs with cache", Parallel and Distributed Processing Symposium Workshops & PhD Forum (IPDPSW), 2012 IEEE 26th International , vol., no., pp.2384-2393, 21-25 May 2012 *Number of citations:12*
- 45 Pei Liu, Kolin Paul, Ahmed Hemani, "Improved Bioinformatics Processing Unit for Multiple Applications", (Parallel and Distributed Processing Symposium Workshops & PhD Forum (RAW), 2012 IEEE 26th International , vol., no., pp.390-396, 21-25 May 2012 *Number of citations:3*
- 44 Mohammad Asad, Liang Guang, Axel Jantsch, Kolin Paul, Ahmed Hemani, Hannu Tenhunen, "Self-Adaptive NOC Power Management With Dual-Level Agents: Architecture and Implementation", SANES (PECCS) 2012, Italy. *Number of citations:5*
- 43 Rajeswari Devadoss, Kolin Paul, M. Balakrishnan, "Architecture and tools for programmable QCA", FPT 2011 Delhi
- 42 Syed. M.A.H. Jafri, Ahmed Hemani, Kolin Paul, Juha Plosila, Hannu Tenhunen, "Compact Generic Intermediate representation (CGIR) to enable late binding in Coarse Grained Reconfigurable Architectures", FPT2011 Delhi *Number of citations:22*.
- 41 Pei Liu, Fatemeh O. Ebrahim, Kolin Paul and Ahmed Hemani, "A Coarse-Grained Reconfigurable Processor for Sequencing and Phylogenetic Algorithms in Bioinformatics", ReConFig 2011 Cancun.
- 40 Tapas Kumar Kundu, Kolin Paul, "Improving Android Performance and Energy Efficiency," pp.256-261, 2011 24th International Conference on VLSI Design, 2011 *Number of citations:7*
- 39 Pei Liu, Ahmed Hemani, Kolin Paul, "A Reconfigurable Processor for Phylogenetic Inference," pp.226-231, 2011 24th International Conference on VLSI Design, 2011
- 38 Syed. M.A.H. Jafri, Ahmed Hemani, Kolin Paul, Juha Plosila, Hannu Tenhunen, "Compression Based Efficient and Agile Configuration Mechanism for Coarse Grained Reconfigurable Architectures," pp.290-293, 2011 IEEE International Symposium on Parallel and Distributed Processing Workshops, 2011 *Number of citations:14*
- 37 Kolin Paul, Tapas Kumar Kundu, "Android on Mobile Devices: An Energy Perspective", CIT 2010: 2421-2426 *Number of citations:87*

- 36 Sohan Lal, Kolin Paul and James Gomes, "Accelerating Metabolic Pathways Simulation using GPUs", ADPC 2010.
- 35 Rajeswari Devadoss, Kolin Paul, M. Balakrishnan: A tiled programmable fabric using QCA. FPT 2010: 9-16
- 34 A High-Level Synthesis Flow for Custom Instruction Set Extensions for Application Specific Processors. Nagaraju P, P Brisk, P lenne, A Kumar and Kolin Paul, ASPDAC 2010. *Number of citations:20.*
- 33 Clocking-based Coplanar Wire Crossing Scheme for QCA. Rajeswari D, Kolin Paul, M Balakrishnan. VLSID 2010 *Number of citations:12.*
- 32 Kolin Paul and Tapas Kundu. Android on Mobile Devices: An Energy Perspective. V International Symposium on Advanced Topics on Embedded Systems and Applications. Bradford, UK, 29 June - 1 July, 2010
- 31 Rohan Paul, Ankush Garg, Vaibhav Singh, Dheeraj Mehra, M. Balakrishnan, Kolin Paul, Dipendra Manocha. Smart Cane for the Visually Impaired: Design, Implementation and Field Tesing of an affordable Obstacle Detection System. TRANSED 2010(Best Paper) *Number of citations:5.*
- 30 Rohan Paul, Ankush Garg, Vaibhav Singh, Dheeraj Mehra, M. Balakrishnan, Kolin Paul, Dipendra Manocha. User Triggered Bus Identification and Homing System: Making public transport accessible for the Visually Challenged. TRANSED 2010.
- 29 Reliable Context Detection for Improving Positioning Performance and Enhancing user Experience. M. Chowdhary, M. Chansarkar, M. Sharma, A. Kumar, K. Paul, M. Jain, C. Agarwal and G. Narula, ION GNSS 2009 Conference *Number of citations:5*
- 28 Rajeswari D, Kolin Paul and M Balakrishnan. Clocking-based Coplanar Wire Crossing Scheme for QCA. International Workshop on Quantum-Dot Cellular Automata 2009
- 27 A Jain and P Gambhir and P Gupta and M Balakrishnan and K. Paul, "FPGA Accelerator for Protein Structure Prediction Algorithms" SPL 2009 *Number of citations:14.*
- 26 Nagaraju P, Anshul Kumar and Kolin Paul. A Novel Approach to Compute Spatial Reuse in the Design of Custom Instructions. VLSID 2008. Hyderabad January, 2008.
- 25 Nagaraju P, Anshul Kumar and Kolin Paul. Exhaustive Enumeration of Legal Custom Instructions for Extensible Processors VLSID 2008. Hyderabad January, 2008 *Number of citations:9.*
- 24 Neeraj Goel and Kolin Paul. Hardware Controlled and Software Independent Fault Tolerant FPGA Architecture ADCOM 2007 Guwahati.
- 23 Kolin Paul Joel Porquet. Silicon Compaction/Defragmentation for Partial Runtime Reconfiguration. EUROMICRO Conference of Digital Systems Design 2007. August 27-31 2007. Lubeck Germany.
- 22 M. Balakrishnan, Kolin Paul, Rohan Paul, Ankush Garg, Vaibhav Singh, Dheeraj Mehra, Dipendra Manocha. PVM Rao, V Singh, V Goel, D Mukherjee. Cane Mounted Knee Above Obstacle Detection and Warning System for the Visually Impaired. 3rd ASME/IEEE Conference on Mechatronics and Embedded Systems and Applications ASME-DETC 2007. September 4-7 2007, Las Vegas.

- 21 Nagaraju Pothineni Anshul Kumar Kolin Paul. Recurring Pattern Identification and its Application to Instruction Set Extension. The 2007 International Conference on Computer Design (ICCD) Las Vegas, Nevada, USA (June 25-28) 2007.
- 20 Rohan Paul, Ankush Garg, Vaibhav Singh, Dheeraj Mehra, M. Balakrishnan, Kolin Paul, Dipendra Manocha. "'Smart' Cane for the Visually Impaired: Technological Solutions for Detecting Waist-above Obstacles" TRANSED 2007 Montreal Canada June 2007
- 19 Nagaraju Pothineni Anshul Kumar Kolin Paul, "Application Specific Datapath Extension with Distributed I/O Functional Units", VLSID 2007. Bangalore January, 2007 *Number of citations:37*.
- 18 Kolin Paul M Balakrishnan. "Experiences of a Summer Workshop in Embedded Systems", Workshop on Embedded System Education, EMSOFT, South Korea . October, 2006.
- 17 Nilesh Padhariya, Kolin Paul and Dheeraj Bhardwaj. "A FLOPS Based Model for Performance Analysis and Scheduling of Applications for Single and Multiple CPUs", ICPP Workshops 2006.
- 16 Neeraj Goel and Kolin Paul. "Fault Tolerant FPGA using Redundant Columns", Proc of VLSI Design and Test Symposium(VDAT 2006), Goa, India.. July, 2006.
- 15 Rahul Jain, Anindita Mukherjee and Kolin Paul, "Defect-Aware Design Paradigm for Reconfigurable Architectures", IEEE Computer Society Annual IEEE Computer Society Annual Symposium on VLSI (ISVLSI 2006). April, 2006 *Number of citations:24*.
- 14 H. Dhand, and N.Goel and M.Agarwal, H. Dhand, N.Goel, M.Agarwal and K.Paul, "Partial and Dynamic Reconfiguration in Xilinx FPGAs — A Quantitative Study", VDAT 2005. August, 2005.
- 13 Sanjay Rajopadhye and Kolin Paul, "A 1.5-D Architecture for Back Propagation Training ", Engineering Of Reconfigurable Systems and Algorithms (ERSA'05) . June, 2005.
- 12 Clare Hugget and Koushik Maharatna and Kolin Paul, "On the Implementation of 128-bit FFT/IFFT for High Performance WPAN", 2005 IEEE International Symposium 2005 IEEE International Symposium on Circuits and Systems. May, 2005 *Number of citations:5*.
- 11 Kolin Paul, "An FPGA based Test Bed for Bio Inspired Computation", 12th Reconfigurable Architectures Workshop RAW 2005, Denver. April, 2005.
- 10 K Paul and S Rajopadhye , "A New Hardware/FPGA Implementation of the Back Propagation Algorithm" IEEE Annual Symposium on VLSI 2003
- 9 P. P. Chaudhuri, B. Sikdar, and K. Paul, "Tutorial on Theory and Application of Cellular Automata CA for VLSI Design and Test", in VLSID'00 INDIA, January 2000. *Number of citations:21*.
- 8 K. Paul, D. R. Chowdhury, and P. P. Chaudhuri, "Scalable Pipelined Micro-Architecture for Wavelet Transform", in Proc. of VLSID'00, INDIA, January 2000.
- 7 K. Paul and D. R. Chowdhury, "Application of $GF(2^p)$ CA in Burst Error Correcting Codes", in Proc. of VLSID'00, INDIA, January 2000 *Number of citations:16*.
- 6 K. Paul, S. P. Chaudhuri, R. Ghosal, B. Sikdar, and D. R. Chowdhury, " $GF(2^p)$ CA Based Vector Quantization for Fast Encoding of Still Images", in Proc. of VLSID'00 INDIA, January 2000. *Number of citations:4*

- 5 B. Sikdar, K. Paul, G. P. Biswas, C. Yang, V. Bopanna, S. Mukherjee, and P. P. Chaudhuri, "Theory and Application of $GF(2^p)$ Cellular Automata as On-Chip Test Pattern Generator", in Proc. of VLSI'00 INDIA, January 2000 *Number of citations:19*.
- 4 K. Paul, D. R. Chowdhury, and P. P. Chaudhuri, "Cellular Automata Based Transform Coding for Image Compression", in Proc. of HiPC'99 INDIA, December 1999 *Number of citations:9*.
- 3 K. Paul, P. Dutta, D. R. Chowdhury, P. K. Nandi, and P. P. Chaudhuri, "A VLSI Architecture for On-Line Image Decompression using $GF(2^p)$ Cellular Automata", in Proc. of VLSID'99 INDIA, January 1999.
- 2 K. Paul, A. Roy, P. K. Nandi, B. N. Roy, M. D. Purkhayastha, S. Chattopadhyay, and P. P. Chaudhuri, "Theory and Application of Multiple Attractor Cellular Automata for Fault Diagnosis", in Proc. of ATS'97 Singapore, December 1997 *Number of citations:14*.
- 1 K. Paul, D. R. Chowdhury, and P. P. Chaudhuri, "A Parallel Architecture for Generation of Fractals", in Proc. of ADCOM'97 INDIA, January 1997.

Patents

- Cuffless Blood Pressure Monitor With Multiple Inertial Measurement Units
Status: U.S. Patent Appl. No. 18/063,021, Filed: December 7, 2022
- Smart Cane – This is a device for helping visually challenged persons to navigate independently.
Status: Provisional patent granted
- Bus Identification Module – This is a two way system which helps a visually challenged person to identify, orient and board a (public transport) bus. The solution is also customizable to build navigational aids for such persons inside buildings.
Status: Provisional patent granted
- Methods and applications for altitude measurement and fusion of user context detection with elevation motion for personal navigation systems
Status: US Patent :US8566032 B2
- Methods and applications for motion mode detection for personal navigation systems
Status: US Patent :US 20110029277 A1
- Neuro-endoscope box trainer
Status: US Patent : US 20170316720 A1
- Neuro-drill-stencil trainer
Status: US Patent : US 20170330485 A1